



Attorney's Docket No. 42P16779

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)
Knauerhase, Robert)
Application No.: 10/609,202)
Filed: June 26, 2003)
For: Virtual Machine Management Using)
Processor State Information)

)

Examiner: Not Yet Assigned

Art Unit: 2127

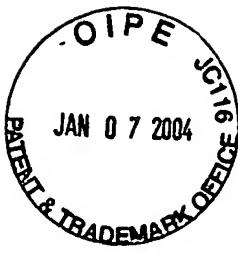
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: January 5, 2004


Aslam A. Jaffery
Reg. No. 51,841

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025
(408) 720-8300

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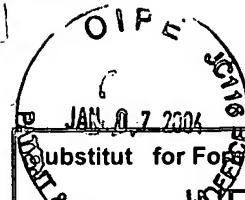
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		'IDE-DE4217444	12/03/1992	Toyohisa, Imada , et al.		
		'EP-EP0473913	03/11/1992	Farrell, Joel A.		
		EP-EP0600112	06/08/1994	Wimmer, Manfred D.		
		'EP-EP0930567	07/21/1999	Brewer, Jason M.		
		EP-EP1030237	08/23/2000	Proudler, John , et al.		

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Application Number	10/609,202
Filing Date	June 26, 2003
First Named Inventor:	Knauerhase, R.
Art Unit	2127
Examiner Name	

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	EP-EP1146715		10/17/2001	Wang, Xin , et al.		
	JP-JP2000076139		03/14/2000	Tanno, Masaaki , et al.		
	WO-WO0062232		10/19/2000	Headrick, Samuel P., et al.		
	WO-WO0127723		04/19/2001	Vamvakas, Athanasia , et al.		
	WO-WO0127821		04/19/2001	Chen, Liqun , et al.		
	WO-WO0175564		10/11/2002	Herbert, Howard C., et al.		
	WO-WO0175565		10/11/2001	Ellison, Carl M.		
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	WO-WO02086684		10/31/2002	Proudler, Graeme J.		
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	WO-WO9834365		08/06/1998	Devanbu, Premkumar T., et al.		
	WO-WO9844402		10/08/1998	Bramhill, Ian D., et al.		
	WO-WO9905600		02/04/1999	Garst, Blaine , et al.		
	WO-WO9909482		01/15/2002	Bausch, Jean		
	WO-WO9957863		11/11/1999	Hayes Jr., Kent F., et al.		

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NON PATENT LITERATURE DOCUMENTS

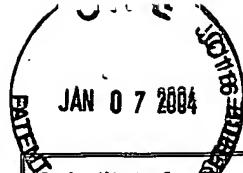
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		BERG, CLIFF , "How Do I Create a Signed Applet?", <u>Dr. Dobb's Journal</u> , (08/1997),1-9	
		BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", <u>SPRINGER-VERLAG</u> <u>XP002201306</u> , (1995),Chapter 3	
		CHEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", <u>7th Annual IEEE Symposium, FCCM '99 Proceedings</u> , <u>XP010359180</u> , ISBN 0-7695-0375-6, Los Alamitos, CA, (4/21/1999),209-221.	
		COMPAQ COMPUTER CORPORATION, et al., "Trusted Computing Platform Alliance (TCPA) Main Specification Version 1.1a", (12/2001),1-321	
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy</u> , IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
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		GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual Machines", <u>IBM J. Research Development</u> , Vol 27, Number 6, (11/1983),530-544	
		HEINRICH, JOE , "MIPS R4000 Microprocessor User's Manual, Second Edition", <u>Chapter 4 "Memory Management</u> ", (6/11/1993),61-97	
		IBM, "Information Display Technique for a Terminate Stay Resident Program IBM Technical Disclosure Bulletin", <u>TDB-ACC-No. NA9112156</u> , Vol. 34, Issue 7A, (12/1/1991),156-158	
		INTEL, "Intel386 DX Microprocessor 32-Bit CMOS Microprocessor With Integrated Memory Management", (1995),5-56	

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		KARGER, PAUL A., et al., "A VMM Security Kernel for the VAX Architecture", <u>Proceedings of the Symposium on Research in Security and Privacy</u> , XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	
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		LAWTON, KEVIN , et al., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques", <u>http://www.plex86.org/research/paper.txt</u> , (11/29/1999),1-31	
		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", <u>IEEE AES Systems Magazine</u> , XP002190614,(March 1999),	
		MENEZES, OORSCHOT , "Handbook of Applied Cryptography", <u>CRC Press LLC, USA</u> XP002201307, (1997),475	
		MOTOROLA, "M68040 User's Manual", (1993),1-1 to 8-32	
		RICHT, STEFAN , et al., "In-Circuit-Emulator Wird Echtzeittauglich", <u>Elektronik, Franzis Verlag GMBH, Munchen, DE</u> , Vol. 40, No. 16, XP000259620,(100-103),8-6-1991	
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", <u>Proceedings of the 9th USENIX Security Symposium</u> , XP002247347, Denver, Colorado, (8/14/00),1-17	
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